

Trie Network Lookup

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ABSTRACT

Networks and routing have become an integral part of life on earth. Networked computing works on routing the packets around the network. The experimentist proposes a new algorithm to reduce the time taken to find the next hop in the lookup table by implementing a partitioned and encoded search mechanism which reduces the time taken from n to square root of n in look-up. This is done by using a trie data structure for the network address encoded in Huffman codes. The class-wise multithreading also helps in achieving faster results. The new algorithm is to improve the time efficiency.

Keywords: Look-up table, Network address, Next hop, Trie data structure, Multi-threading, Huffman Code, Router.

1. INTRODUCTION

Computer networks is an important aspect in our daily progress as we depend on networked computers for almost everything nowadays. The primary role of routers is to forward packets towards their final destination. To this purpose, a router must decide for each incoming packet where to send it next. the next-hop router as well as the egress port through which the packet should be sent. This forwarding information is stored in a forwarding table that the router computes based on the information gathered by routing protocols. To consult the forwarding table, the router uses the packet's destination address as a key; this operation is called address lookup. Once the forwarding information is retrieved, the router can transfer the packet from the incoming link to the appropriate outgoing link, in a process called switching. The address lookup operation is a major bottleneck in the forwarding performance of today's routers. This paper presents a technique to improve the efficiency of the existing look up algorithm.

2. EXPLANATION

In IP version 4, IP addresses are 32 bit long and, when broken up into 4 groups of 8 bits, are normally represented as four decimal numbers separated by dots. For example, the address 1000010_01010110_00010000_01000010 corresponds in the dotted-decimal notation to 130.86.16.66. IP addresses use a two level hierarchy, with network layer on top and the host layer at the bottom. Routing protocols mostly focus on the network part of the addresses which is called the address prefix a bit forms the first few bits of the IP address. With a two-level hierarchy, IP routers forwarded packets based only on the network part, until packets reached the destination network. As a result, a forwarding table only needed to store a single entry to forward packets to all the hosts attached to the same network. This technique is called address aggregation and allows using prefixes to represent a group of addresses. Each entry in a forwarding table contains a prefix, as can be seen in Table 1. So, finding the forwarding information requires to search for the prefix in the forwarding table that matches the corresponding bits of the destination address.

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Table 1
A forwarding table

<i>Destination Address Prefix</i>	<i>Next-hop</i>	<i>Output interface</i>
24.40.32/20	192.41.177.148	2
130.86/16	192.41.177.181	6
208.12.16/20	192.41.177.241	4
208.12.21/24	192.41.177.196	1
167.24.103/24	192.41.177.3	4

The addressing architecture specifies how the allocation of addresses is performed, that is it define show to partition the total IP address space of 232 addresses. Specifically, how many network addresses will be allowed and of what size each of them should be. When the Internet addressing was initially designed, a rather simple address allocation scheme was defined, which is known today as the classful addressing scheme. Basically, three different sizes of networks were defined in this scheme, identified by a class name: class A, B, and C (see figure 1). Size of networks was determined by the number of bits used to represent the network part and the host part. Thus networks of class A, B or C consisted in an 8, 16 or 24-bit network part and a corresponding 24, 16 or 8-bit host part.

With this scheme there were very few class A networks and their addressing space represented 50% of the total IPv4 address space (231 addresses out of a total of 232). There were 16,384 (2¹⁴) class B networks with a maximum of 65,534 hosts per network and 2,097,152 (2²¹) class C networks with up to 256 hosts. This allocation scheme worked well in the early days of the Internet. However, the continuous growth of the number of hosts and networks have made apparent two problems with the classful addressing architecture. First, with only three different network sizes to choose, the address space was not used efficiently and the IP address space was getting exhausted very rapidly, even though only a small fraction of the addresses allocated were actually in use. Second, although the state information stored in the forwarding tables did not grow in proportion to the number of hosts, it still grew in proportion to the number of networks. This was especially important in the backbone routers, which must maintain an entry in the forwarding table for every allocated network address. As a result, the forwarding tables in the backbone routers were growing very rapidly. The growth of the forwarding tables resulted in higher lookup times and higher memory requirements in the routers and threatened to impact their forwarding capacity.

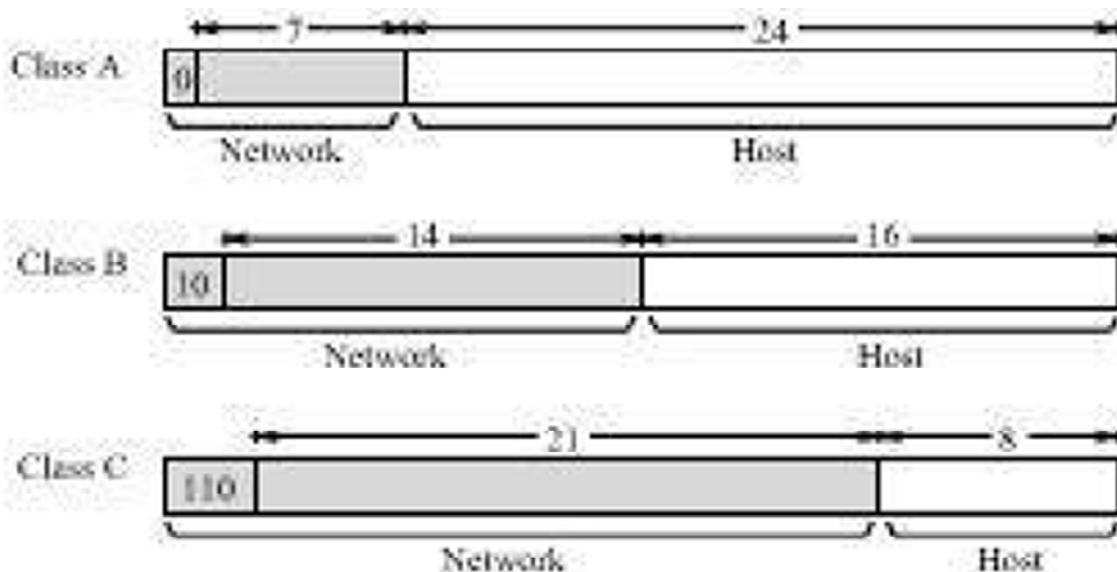


Figure 1: Classful Addresses

square root of the number of entries in the look up table. The encoding and decoding when done in Huffman codes can make sure that memory usage is reduced. The usage of multi-threaded search algorithms need better hardware so cost of production will be higher than usual but time efficiency will be considerably increased.

The multi-threaded algorithm is as follows

- a) Split the IP address into three parts, first part being the first octant, second part being the rest of the IP address and the last part being the host address.
- b) Neglect the host address part and consider only the second part.
- c) Encode the second part in Huffman codes and construct binary tries with the next hops appended on the node.

This considerably improves the efficiency of the search and also the memory complexity of the routing mechanisms.

4. RESULT

Routing in networks is a very important process. With the increasing network addresses and number of hosts the time taken to route a packet is crucial. The proposed algorithm when implemented has proven to reduce the time complexity of routing algorithms from $O(n)$ to $O(d)$ by multithreading and encoding in Huffman codes.

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