

# Comparison of Responses of the PI and the PID Controlled Closed Loop DPFC Systems

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**Abstract:** The proposed work deals with comparison of the PI and the PID controlled closed loop DPFC systems. The voltage across the load decreases due to the addition of extra load and the load voltage is restored back to normal value by using closed loop system. The ability of closed loop system to bring the voltage and reactive power back to the set value is represented in this paper. The simulation studies for open loop and closed loop systems are performed on a standard ten bus radial test system.

## 1. INTRODUCTION

Power Quality is becoming an important issue for both electric utilities and end users [1]. Unbalanced voltages and currents in a network are one of the concerns under the power quality issue. The unbalance is mainly produced by the great number of single-phase loads which are unevenly distributed over the phases [2]. The unbalance voltages can cause extra losses in components of the network, such as generators, motors and transformers, while unbalanced currents cause extra losses in components like transmission lines and transformers [3]. Active filters and power factor corrector can be applied to compensate the unbalance at the load side.

However their contribution to transmission systems is not large because they are focused on single load [4], [5]. FACTS devices can be employed to compensate the unbalanced currents and voltages in transmission systems. Unfortunately, it is found that the capability of most of FACTS devices to compensate unbalance is limited. Series and shunt FACTS device can only provide compensation of unbalanced reactive currents [6], and the most powerful device – the UPFC [7] cannot compensate zero-sequence unbalance current, because of the converter topology [8]. DPFC can compensate both active and reactive powers. The zero and negative sequence unbalanced currents can also be compensated. The Distributed Power Flow Controller (DPFC) recently presented in [9], is a powerful device within the family of FACTS devices, which provides much lower cost and higher reliability than conventional FACTS devices. It is derived from the UPFC and has the same capability of simultaneously adjusting all the parameters of the power system such as line impedance, transmission angle, and bus voltage magnitude [7]. Within the DPFC, the common DC link between the shunt and series converters is eliminated, which provides flexibility for independent placement of series and shunt converters. The DPFC uses the transmission line to exchange active power between converters at the third harmonic frequency [9]. Instead of one large three-phase converter, the DPFC employs multiple single-phase converters (D-FACTS concept [10]) as the series compensator. This concept not only reduces the rating of the components but also provides a high reliability because of the redundancy. The scheme of the DPFC in a simple two-bus System is illustrated in Fig.1.

As the series converters of the DPFC are single-phase, it gives the DPFC the opportunity to control current in each phase independently, which implies that both negative and zero sequence unbalanced current can be compensated. Additional controllers are supplemented to the existing DPFC controller. Their control

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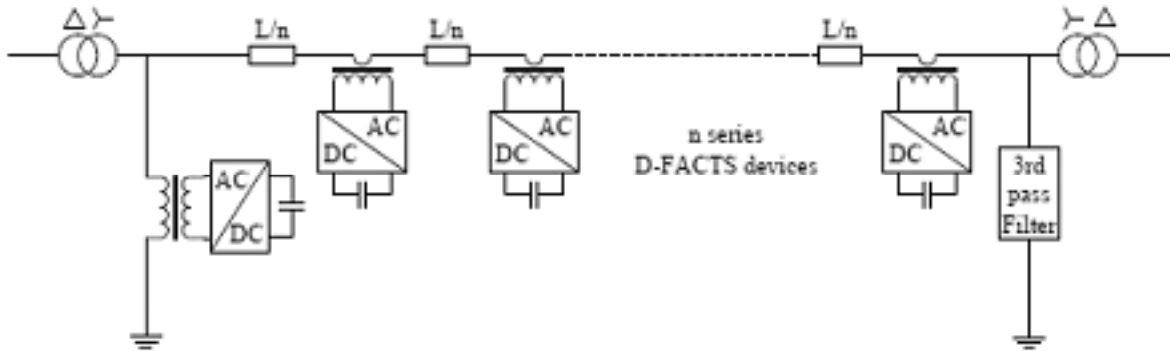


Figure 1: Distributed power flow controller

principle is to monitor the negative and zero sequences current through the transmission line and to force them to zero. The above literature does not deal with closed loop controlled DPFC system using PI or PID controllers. The objective of this paper is to study the improvement in dynamic response using PID controller.

## 2. PRINCIPLE OF THE DPFC

### 2.1. Introduction of the DPFC

Multiple individual converters cooperate together and compose the DPFC, see Fig (1). The converters connected in series with the transmission lines are the series converters. They can inject a controllable voltage at the fundamental frequency; consequently they control the power flow through the line. The converter connected between the line and ground is the shunt Converter. The function of the shunt converter is to compensate reactive power to the grid, and to supply the active power required by the series converter. In a normal UPFC, there is active power exchange through the DC link that connects the series converter with the shunt converter. Since there is no common DC link between the shunt and series converters in the DPFC, the active power is exchanged by harmonics and through the AC network. The principle is based on the definition of active power, which is the mean value of the product of voltage and current, where the voltage and current comprise fundamental and harmonics. Since the integrals of all the cross-product of terms with different frequencies are zero, the time average active power can be expressed by:

$$P = \sum_{n=1}^{\infty} V_n I_n \cos \phi_n \quad (1)$$

Where  $n$  is the order of the harmonic frequency and  $\phi_n$  is the angle between the current and voltage of the  $n$ th harmonic. Equation 1 describes that active powers at different frequencies are isolated from each other and that voltage or current in one frequency component has no influence on other frequency components. The 3rd harmonic is chosen here to exchange the active power, because it can easily be filtered by using Y- $\Delta$  transformers.

### 2.2. Control Principle of DPFC

The DPFC system consists of two types of converters, and each type of converter requires a different control scheme. The block diagram of the DPFC and its control are shown in Fig (2). The shunt converter is controlled to inject a constant third harmonic current into the transmission line, which is intended to supply active power for the series converters.

The shunt converter extracts some active power from the grid at the fundamental frequency to maintain its DC voltage. The DC voltage of the shunt converter is controlled by the  $d$  component of the current at the fundamental frequency, and the  $q$  component is utilized for reactive power compensation. The series converters generate a voltage with controllable phase angle at fundamental frequency, and use the voltage

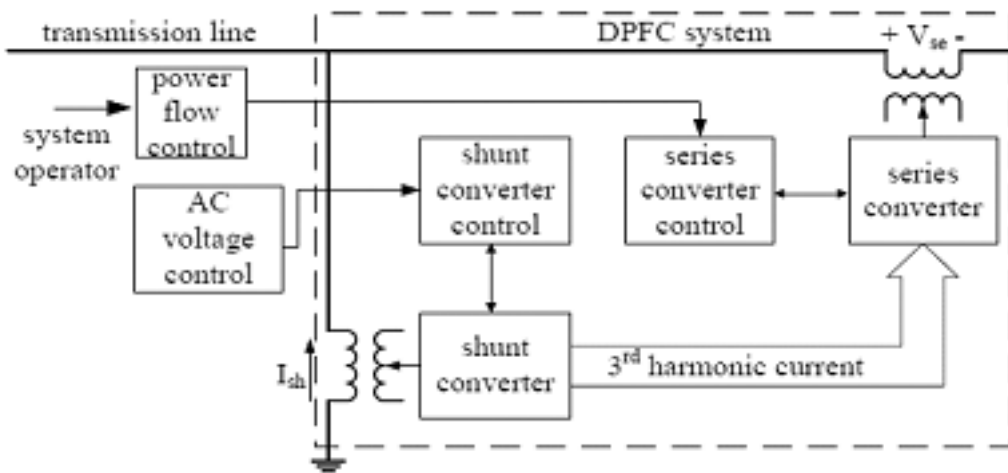


Figure 2: Block diagram of the control of a DPFC

at the third harmonic frequency to absorb active power to maintain its DC voltages at a constant value. The power flow control function is realized by an outer control loop, the power flow control block. This block gets its reference signals from the system operator, and the control signals for DPFC series converters are sent remotely via wireless or PLC communication method.

The functions of each control block shown in Fig (2) are described here:

- **Power flow control:** It receives the set point for power flow from the system operator, and calculates the fundamental frequency voltage that should be injected by the series converters.
- **Series converter control:** It generates switching signals according to the received data, and stabilizes DC capacitor voltage by controlling third harmonic components.
- **AC voltage control:** gives the set points to shunt converter for reactive power compensation at the fundamental frequency.
- **Shunt converter control:** generates 3rd harmonic current, the reactive current at the fundamental frequency and stabilizes the DC voltage.

### 3. SIMULATION RESULTS

#### 3.1. Open loop controlled DPFC system

Open loop controlled DPFC system is shown in Fig 3(a). A shunt converter is connected in parallel with the alternator at the sending end. Two inverters inject the voltage in series with the line as shown in Fig 3(a).

Additional load can be added to the existing load by using a breaker. The voltage at the receiving end is shown in Fig 3(b).

At  $t = 0.9$  sec, the second load is connected. The voltage decreases from 6350 V to 6210V. The reactive power also decreases as shown in Fig 3(c).

#### 3.2. Closed loop controlled DPFC system with PI Controller

Closed loop reactive power controlled system using PI controller is shown in Fig 4(a). The real and reactive powers are measured using PQ block.

Actual reactive power is compared with the reference reactive power and the error is applied to the PI controller. The control circuit increases the pulse width so that the voltage at the receiving end is improved. The voltage at the receiving end is shown in Fig 4(b).

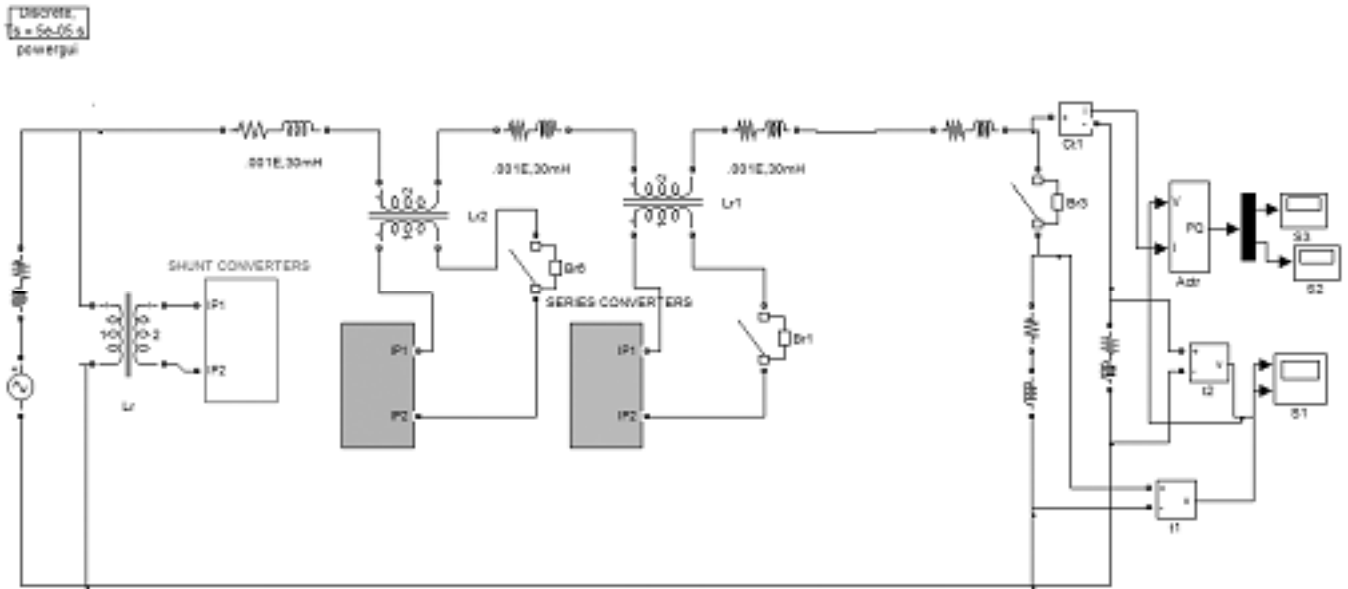


Figure 3(a): Open loop controlled DPFC System

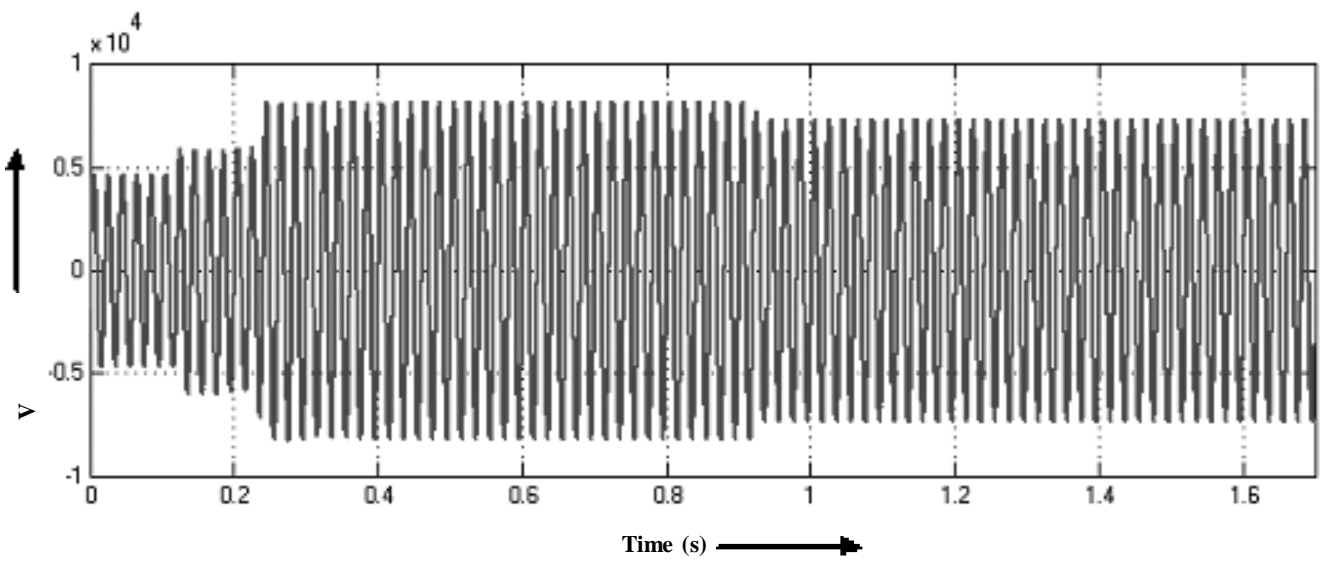


Figure 3(b): Voltage at the Receiving end

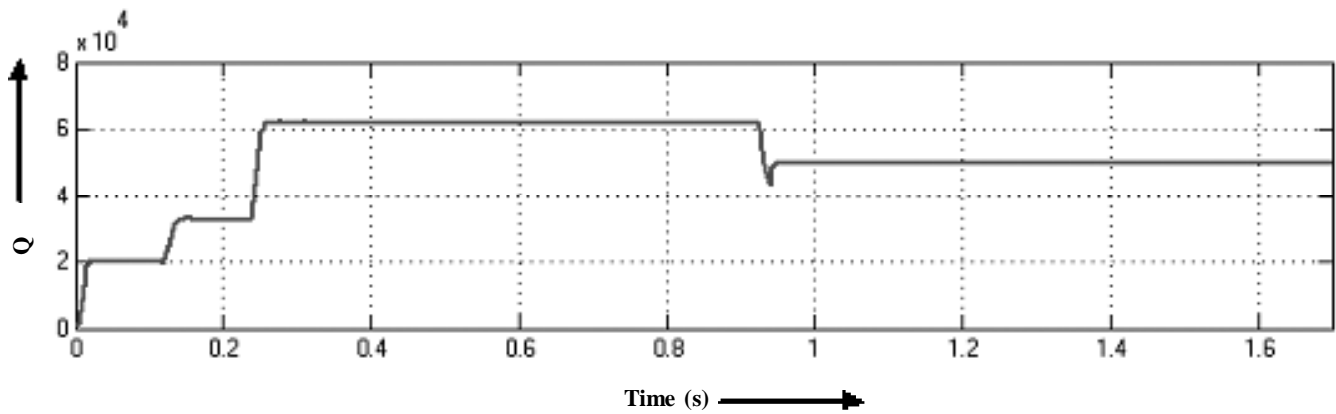


Figure 3(c): Reactive power

Discrete  
s = 5e-05 s  
powergui

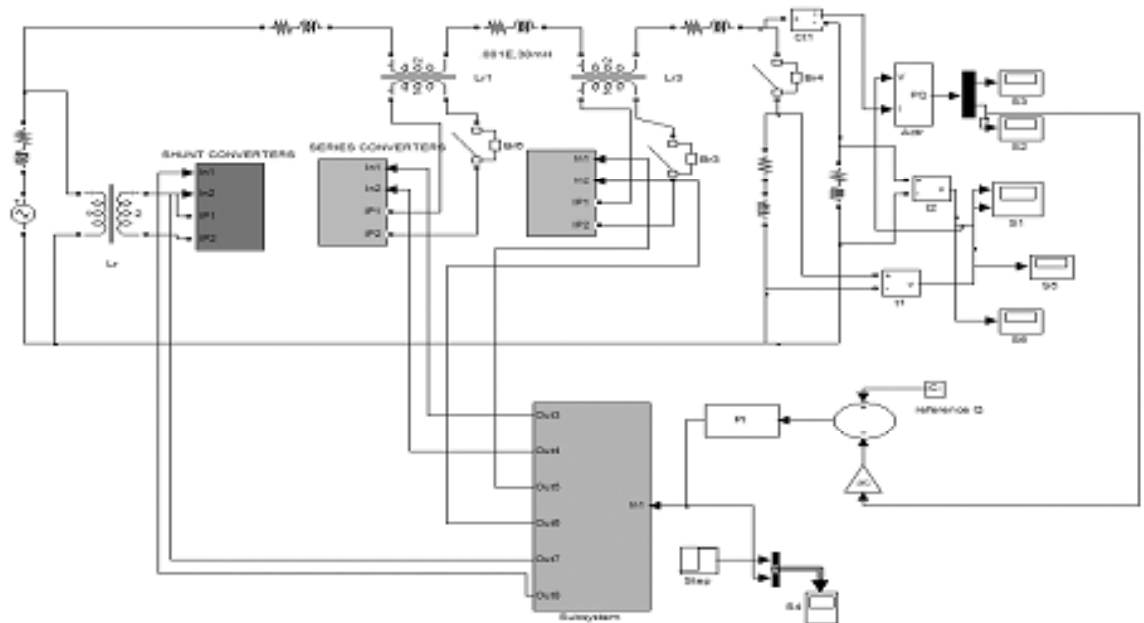


Figure 4(a): Closed Loop System with the PI Controller

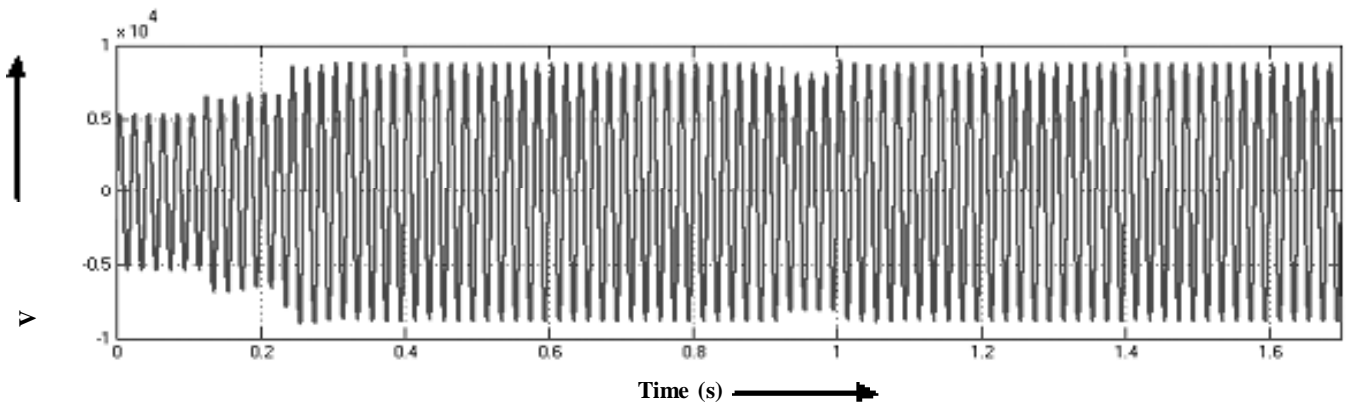


Figure 4(b): Voltage at the receiving end

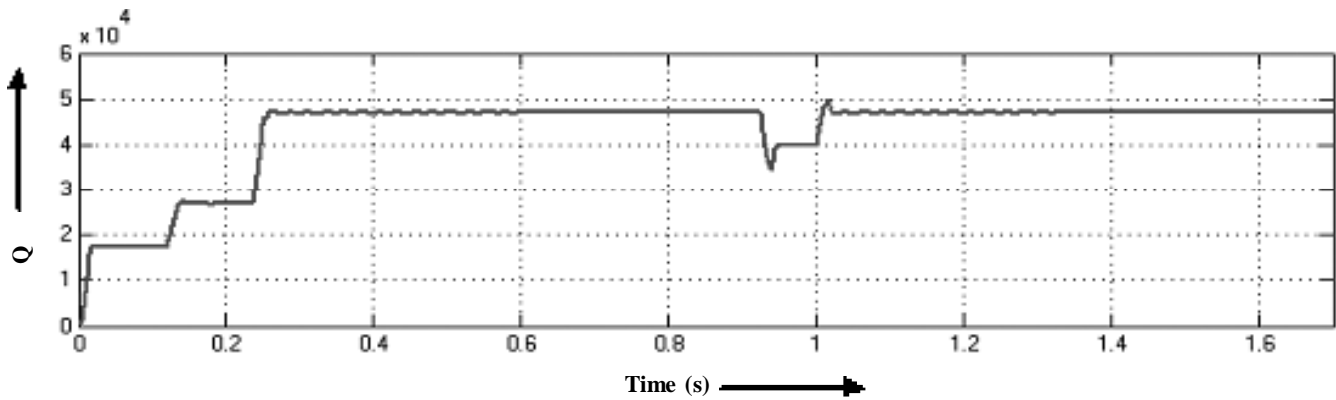


Figure 4(c): Reactive power

The voltage initially decreases and then reaches normal value due to the action of the closed system. The voltage reaches normal value at  $t = 1$  sec. The variation of reactive power is shown in Fig 4(c). The reactive power decreases and then reaches the set value.

**3.3. Closed loop controlled DPFC system with the PID Controller**

Closed loop controlled DPFC system with PID controller is shown in Fig 5(a).

The receiving end voltage is shown in Fig 5(b).

The voltage decreases and then gets corrected due to the increased pulse width of injected voltage. The variation of reactive power is shown in Fig 5(c). The reactive power resumes to the normal value as shown in Fig 5(c).

DISCRETE  
Ts = 5e-05 s  
powergui

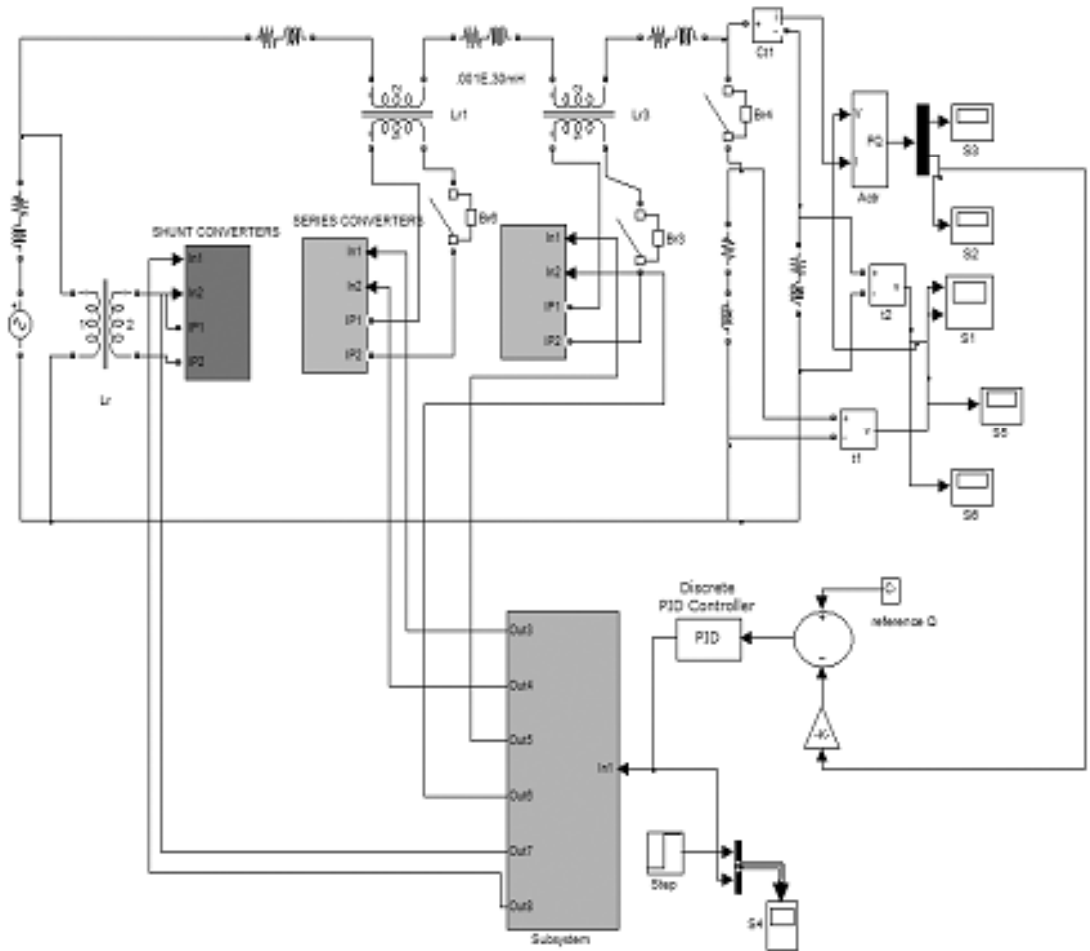


Figure 5(a): Closed loop system with the PID controller

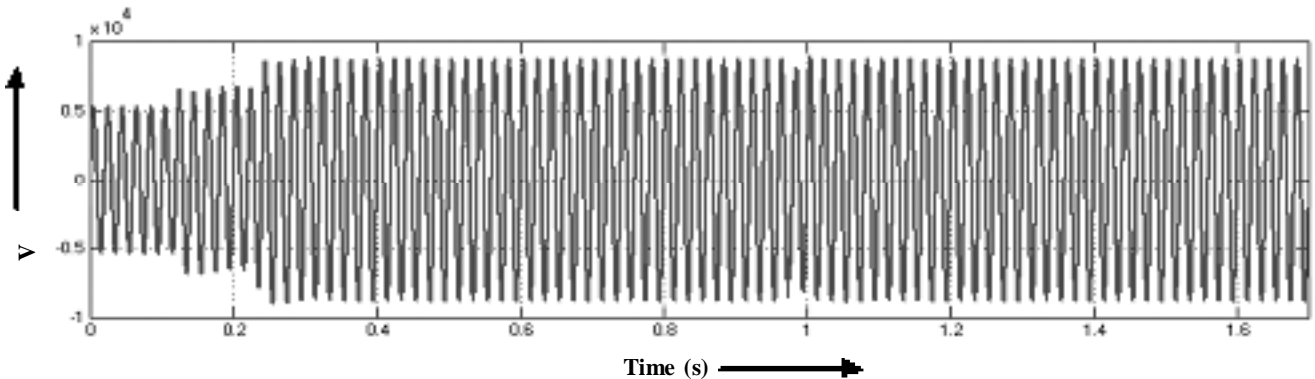


Figure 5(b): Voltage at the receiving end

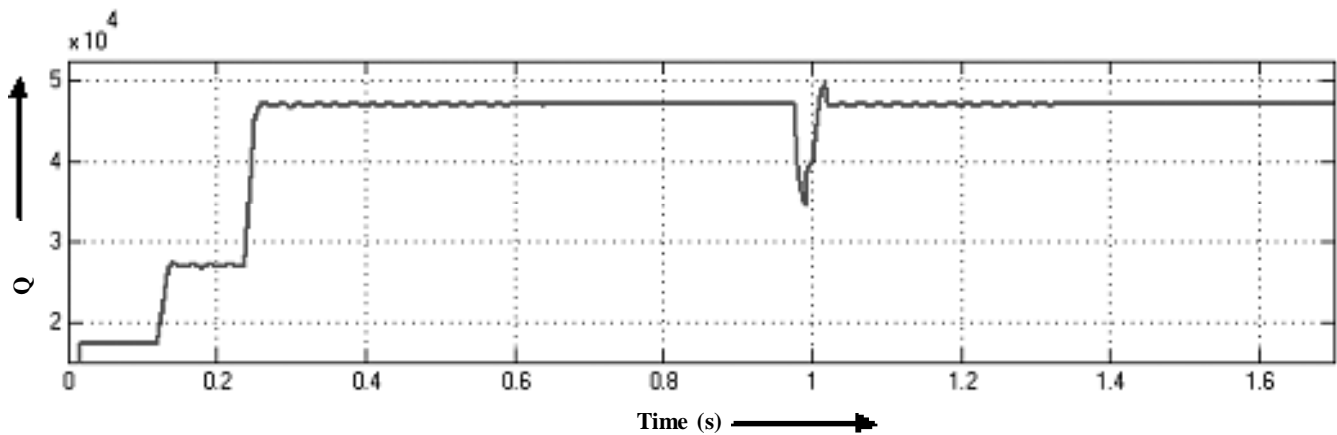


Figure 5(c): Reactive power

The comparison of various parameters with the PI & the PID controllers is given in Table 1. It can be seen that the peak overshoot is reduced from 1.1V to 0.8V using PID controller. All the parameters are reduced by using PID controllers. The power settles quickly by using PID controller. The steady state error and peak over shoot are reduced by using PID controller.

**Table 1**  
Comparison of responses with the PI & the PID controllers

controller	TsSec	VpVolts	EssVolts
PI controller	1.09	1.1	0.008
PID controller	1.01	0.8	0.004

#### 4. CONCLUSION

DPFC systems controlled by PI & PID controllers were designed, modeled and simulated using Matlab Simulink. The simulation results of open loop and closed loop systems were presented. The proposed reactive power loop was successfully employed to maintain constant reactive power. The response of PID controlled system was found to be superior to the PI controlled system. This was due to reduction in the peak time, the peak overshoot and the steady state error. The advantages of DPFC are improved voltage and reactive power profiles. The disadvantage of DPFC is the requirement of about six inverters, six driver circuits and injection transformers.

The scope of the work is modeling and simulation of reactive power controlled ten bus system using PI & PID controllers. The studies will be extended with fuzzy logic controller to improve the dynamic response.

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